

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	92	(CRC or (cyclic adj1 redundancy adj1 check)) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or"))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:34
L11	50	(CRC or (cyclic adj1 redundancy adj1 check)) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or")) same (register\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:34
L12	19	(CRC or (cyclic adj1 redundancy adj1 check)) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or")) same (register\$1) same polynomial	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:35
L13	7	((CRC or (cyclic adj1 redundancy adj1 check)) near5 generat\$5) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or")) same (register\$1) same polynomial	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:38
L14	7	((CRC or (cyclic adj1 redundancy adj1 check)) near5 generat\$5) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or")) same (register\$1) same polynomial\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:45
L15	7	((CRC or (cyclic adj1 redundancy adj1 check)) adj3 generat\$5) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or")) same (register\$1) same polynomial\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:35
L16	3	((CRC or (cyclic adj1 redundancy adj1 check)) adj3 generat\$5) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or")) same (register\$1) same polynomial\$1 same (select\$4 or multiplex\$3)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:35

L17	7	((CRC or (cyclic adj1 redundancy adj1 check)) near5 generat\$5) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or")) same (register\$1) same polynomial\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:39
L21	1519	cheng-h\$.in.	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:41
L24	10	chen-hongyi.in.	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:44
L25	0	chen-hongyi.in. and crc	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:44
L26	23	((((CRC or (cyclic adj1 redundancy adj1 check)) near5 generat\$5 or calculat\$4 or comput\$4)) same (program\$5 or reprogram\$5 or configur\$5 or reconfigur\$5) same ("xor" or (exclusive adj2 "or")) same (register\$1) same polynomial\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:46
L27	180	((((CRC or (cyclic adj1 redundancy adj1 check)) near5 generat\$5 or calculat\$4 or comput\$4)) same ("xor" or (exclusive adj2 "or")) same (register\$1) same polynomial\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/01 14:47
L28	1	CRC and "pre-selected polynomial"	US-PGPUB	OR	OFF	2005/11/01 14:48


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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information



1. Parallel CRC generation

Albertengo, G.; Sisto, R.;
Micro, IEEE

Volume 10, Issue 5, Oct. 1990 Page(s):63 - 71

Digital Object Identifier 10.1109/40.60527

[AbstractPlus](#) | Full Text: [PDF](#)(916 KB) IEEE JNL

2. Automatic generation of parallel CRC circuits

Sprachmann, M.;
Design & Test of Computers, IEEE

Volume 18, Issue 3, May-June 2001 Page(s):108 - 114

Digital Object Identifier 10.1109/54.922807

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(92 KB) IEEE JNL

3. Fast and flexible CRC calculation

Doering, A.; Waldvogel, M.;
Electronics Letters

Volume 40, Issue 1, 8 Jan. 2004 Page(s):10 - 11

Digital Object Identifier 10.1049/el:20040032

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